Substitute for form 1449APTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known		
				Application Number	Not yet assigned	
				Filing Date	July 14, 2003	
				First Named Inventor	Chiang Pu	
				Art Unit	Not yet assigned	
				Examiner Name	Not yet assigned	
Sheet	1	of	1	Attorney Docket Number	42P16565	

OTHER ART - NON PATENT LITERATURE DOCUMENTS									
Examiner Initials*	Cite No.'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. Su, David K and Wolley, Bruce A. "A CMOS Oversampling D.A converter with a Current-Mode Semidigital Reconstruction Filter," IEEE Journal of Solid-State Circuits, Vol. 28, No. 12, December 1993. Figueroa, Miguel, Hsu, David and Diorio, Chris, "A Mixed-Signal Approach to High-Performance Low-Power Linear Filters," IEEE Journal of Solid-State Circuits, Vol. 36, No. 5, May 2001.							
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Examiner /Tan Mai/	Date 09/13/2006 Considered	
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^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

'Applicant's unique citation designation number. 'Applicant is to place a check mark here if English language Translation is attached.

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